

### ABSTRACT

A liquid crystal display and method adaptive for preventing transition of liquid crystals from a bend state to a splay state caused by an external impact when using a data voltage lower than a transition voltage  $V_{tr}$  while actually driven. In an embodiment, the liquid crystal display includes a liquid crystal display panel having a thin film transistor at each intersection part of a plurality of data lines and a plurality of gate lines; a gate driver configured to supply a gate high voltage and a gate low voltage during a data input period, and sequentially supply a gate reset voltage to gate lines during a reset period, wherein a normal drive period is divided into the data input period and the reset period; a data driver configured to supply data voltages to the data lines in accordance with gate voltages applied to the gate lines; and a timing controller arranged to control the data voltages supplied to the data lines and the gate voltages supplied to the gate lines.